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Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 111778		APPLICATION NO. New U.S. Patent Application	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANTS Akihiko EBINA, Yutaka MARUO			
				FILING DATE January 23, 2002			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
SBG	1	6,255,166 B1	07/03/2001	Ogura et al.	—	—	
SBG	2	5,408,115	04/18/1995	Chang	—	—	
SBG	3	5,969,383	10/19/1999	Chang et al.	—	—	
SBG	4	5,422,504	06/06/1995	Chang et al.	—	—	
SBG	5	5,494,838	02/27/1996	Chang et al.	—	—	
SBG	6	6,177,318 B1	01/23/2001	Ogura et al.	—	—	
SBG	7	6,248,633 B1	06/19/2001	Ogura et al.	—	—	
SBG	8	Docket # 111779	01/23/2002	Ebina et al.	—	—	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
SBG	9	JP B1 2978477 w/ translation	09/10/1999	Japan	—	—	
SBG	10	JP A 7-161851 w/ translation	06/23/1995	Japan	—	—	
SBG	11	JP A 2001-156188 w/ translation	06/08/2001	Japan	—	—	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
SBG	12	Yutaka Hayashi et al., "Twin MONOS Cell with Dual Control Gates", 2000 Symposium on VLSI Technology Digest of Technical Papers, 2000					
SBG	13	Kuo-Tung Chang et al., "A New SONOS Memory Using Source-Side Injection for Programming", IEEE Electron Device Letters, Vol. 19, No. 7, pp. 253-255, 1998					
SBG	14	Wei-Ming Chen et al., "A Novel Flash Memory Device with Split Gate Source Side Injection and ONO Charge Storage Stack (SPIN)", 1997 Symposium on VLSI Technology Digest of Technical Papers, pp 63-64, 1997					
EXAMINER		SAB. B.			DATE CONSIDERED 8/5/03		
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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